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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Applicant Vikas K. Agrawal, et al.

Docket Number: TI-35282

Serial No.: 10/663,575

Art Unit: 2824

Filed: 09/16/03

Examiner: Van Thu T. Nguyen

For: Cycle Ready Circuit for Self-Clocking Memory Device

CERTIFICATION OF FACSIMILE TRANSMISSION

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<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> EOT
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NAME OF INVENTOR(S): Vikas K. Agrawal, et al.	
RECEIPT DATE & SERIAL NO.: Serial No.: 10/663,575	
TITLE OF INVENTION: Cycle Ready Circuit for Self-Clocking Memory Device Filing Date: 09/16/03	
TI FILE NO.: TI-35282	DEPOSIT ACCT. NO.: 20-0668
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Applicant: Vikas K. Agrawal, et al. Art Unit: 2824
Serial No.: 10/663,575 Examiner: Van Thu T. Nguyen
Filed: 09/16/03 Docket: TI-35282
For: CYCLE READY CIRCUIT FOR SELF-CLOCKING MEMORY DEVICE

Commissioner of Patents
P.O. Box 1450
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AMENDMENT

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